



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

10,007,840

TI-30110

Duy Q. Nguyen, et al.

Art Unit: 2661

Serial No:

Examiner:

Filed: October 30, 2001

Conf. No.: 7621

For: External Bus Arbitration Technique for Multicore DSP Device

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Technology Center 2600

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Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,



Robert D. Marshall, Jr.  
Attorney for Applicant(s)  
Reg. No. 28,527

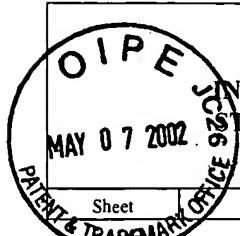
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INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT

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Complete If Known

Application Number	
Filing Date	October 30, 2001
First Named Inventor	Duy Q. Nguyen, et al.
Group Art Unit	2661
Examiner Name	

Sheet

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of

1

Attorney Docket No.

TI-30110

## U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No.1	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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AA						
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		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>2</sup> (if known)				
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## OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
CA		Texas Instruments Incorporated, <i>TMS320VC5421 Fixed-Point Digital Signal Processor – Data Manual</i> , Dec., 1999, pgs. 34-35.	
CB		Winderweedle, Bill; <i>TMS320VC5420 to TMS320VC5421 DSP Migration</i> , Dec., 1999, whole document.	
CC			
CD			
CE			
CF			
CG			
CH			

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